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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Ryu et al. Serial No.: 09/911,995 Filed: July 24, 2001 Group Art Unit: 2811 Confirmation No.: 5240 Examiner: Gene M. Munson

For: SILICON CARBIDE POWER METAL-OXIDE SEMICONDUCTOR FIELD

EFFECT TRANSISTORS HAVING A SHORTING CHANNEL AND METHODS OF FABRICATING SILICON CARBIDE METAL-OXIDE SEMICONDUCTOR FIELD EFFECT TRANSISTORS HAVING A

SHORTING CHANNEL

Date: February 04, 2004

Commissioner for Patents PO Box 1450 Alexandria, VA 22313

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a list of documents on form PTO-1449 together with copies of each identified document. This Information Disclosure Statement is submitted in accordance with 37 C.F.R. § 1.97(c), before final Office Action or Allowance, whichever is earlier.

The Commissioner is authorized to charge \$180.00 for the fee specified in 37 C.F.R. § 1.17(p) and any additional fee or credit any refund to Deposit Account No. 50-0220.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on February 04, 2004.

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